

	L #	Hits	Search Text	DBs
1	L1	933	thread adj2 (id identif\$6)	USPAT; US-PGPUB
2	L2	84803	(data load read miss) near10 (delay latency wait\$3)	USPAT; US-PGPUB
3	L3	260	1 and 2	USPAT; US-PGPUB
4	L4	6827	context near5 (switch\$3 chang\$3)	USPAT; US-PGPUB
5	L6	140	thread adj2 (id identif\$6)	EPO; JPO; DERWENT; IBM_TDB
6	L7	105241	(data load read miss) near10 (delay latency wait\$3)	USPAT; EPO; JPO; DERWENT; IBM_TDB
7	L8	642	context near5 (switch\$3 chang\$3)	EPO; JPO; DERWENT; IBM_TDB
8	L12	176	3 not (5 11)	USPAT; US-PGPUB
9	L5	84	4 and 3	USPAT; US-PGPUB
10	L11	66	1 and 4 not 5	USPAT; US-PGPUB
11	L9	7	6 and 8	EPO; JPO; DERWENT; IBM_TDB
12	L10	4	6 and 7	EPO; JPO; DERWENT; IBM_TDB

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1	L1	933	thread adj2 (id identif\$6)	USPAT; US-PGPUB
2	L2	84803	(data load read miss) near10 (delay latency wait\$3)	USPAT; US-PGPUB
3	L3	260	1 and 2	USPAT; US-PGPUB
4	L4	6827	context near5 (switch\$3 chang\$3)	USPAT; US-PGPUB
5	L6	140	thread adj2 (id identif\$6)	EPO; JPO; DERWENT; IBM_TDB
6	L7	105241	(data load read miss) near10 (delay latency wait\$3)	USPAT; EPO; JPO; DERWENT; IBM_TDB
7	L8	642	context near5 (switch\$3 chang\$3)	EPO; JPO; DERWENT; IBM_TDB
8	L12	176	3 not (5 11)	USPAT; US-PGPUB
9	L5	84	4 and 3	USPAT; US-PGPUB
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11	L9	7	6 and 8	EPO; JPO; DERWENT; IBM_TDB
12	L10	4	6 and 7	EPO; JPO; DERWENT; IBM_TDB

	Docum ent ID	U	Title	Current OR
1	US 20030 11417 4 A1	<input type="checkbox"/>	Mobile telephone short text messaging with message thread identification	455/466
2	US 20030 11041 6 A1	<input checked="" type="checkbox"/>	Methods and systems for creating and communicating with computer processes	714/39
3	US 20030 10594 6 A1	<input checked="" type="checkbox"/>	Broadcast state renaming in a microprocessor	712/245
4	US 20030 09739 6 A1	<input checked="" type="checkbox"/>	METHOD AND APPARATUS FOR CONVERTING A LIGHTWEIGHT MONITOR TO A HEAVYWEIGHT MONITOR	709/104
5	US 20030 09379 2 A1	<input checked="" type="checkbox"/>	Method and apparatus for delivery of television programs and targeted de-coupled advertising	725/46
6	US 20030 07465 0 A1	<input checked="" type="checkbox"/>	Debugger operating system for embedded systems	717/129
7	US 20030 06755 4 A1	<input checked="" type="checkbox"/>	System and method for personalized TV	348/461
8	US 20030 06125 8 A1	<input checked="" type="checkbox"/>	Method and apparatus for processing an event occurrence for at least one thread within a multithreaded processor	709/102
9	US 20030 05620 0 A1	<input checked="" type="checkbox"/>	Runtime monitoring in component-based systems	717/128
10	US 20030 05619 9 A1	<input checked="" type="checkbox"/>	Hyperbolic tree space display of computer system monitoring and analysis data	717/127
11	US 20030 05589 0 A1	<input checked="" type="checkbox"/>	Information processing system	709/203
12	US 20030 03720 3 A1	<input checked="" type="checkbox"/>	Multi-threaded random access storage device qualification tool	711/104
13	US 20030 02868 0 A1	<input checked="" type="checkbox"/>	Application manager for a content delivery system	709/313
14	US 20030 02383 5 A1	<input checked="" type="checkbox"/>	Method and system to perform a thread switching operation within a multithreaded processor based on dispatch of a quantity of instruction information for a full instruction	712/214
15	US 20030 02383 4 A1	<input checked="" type="checkbox"/>	Method and system to insert a flow marker into an instruction stream to indicate a thread switching operation within a multithreaded processor	712/214
16	US 20030 02365 9 A1	<input checked="" type="checkbox"/>	Method and apparatus for thread switching within a multithreaded processor	709/102
17	US 20030 02365 8 A1	<input checked="" type="checkbox"/>	Method and system to perform a thread switching operation within a multithreaded processor based on detection of the absence of a flow of instruction information for a thread	709/102

	Docum ent ID	U	Title	Current OR
18	US 20030 01868 7 A1	<input checked="" type="checkbox"/>	Method and system to perform a thread switching operation within a multithreaded processor based on detection of a flow marker within an instruction information	709/102
19	US 20030 01868 6 A1	<input checked="" type="checkbox"/>	Method and system to perform a thread switching operation within a multithreaded processor based on detection of a stall condition	709/102
20	US 20030 01868 5 A1	<input checked="" type="checkbox"/>	Method and system to perform a thread switching operation within a multithreaded processor based on detection of a branch instruction	709/102
21	US 20030 01455 8 A1	<input checked="" type="checkbox"/>	Batch interrupts handling device, virtual shared memory and multiple concurrent processing device	709/318
22	US 20030 00468 3 A1	<input checked="" type="checkbox"/>	Instruction pre-fetching mechanism for a multithreaded program execution	702/186
23	US 20020 19439 3 A1	<input checked="" type="checkbox"/>	Method of determining causal connections between events recorded during process execution	709/318
24	US 20020 19101 8 A1	<input checked="" type="checkbox"/>	System and method for implementing a graphical user interface across dissimilar platforms yet retaining similar look and feel	345/746
25	US 20020 18440 9 A1	<input checked="" type="checkbox"/>	System and method for introducing enhanced features into a java swing application program interface	709/328
26	US 20020 18079 3 A1	<input checked="" type="checkbox"/>	Dynamic buffering of graphic images by a platform independent application program interface	345/766
27	US 20020 18079 2 A1	<input checked="" type="checkbox"/>	Combining the functionality of multiple text controls in a graphical user interface	345/764
28	US 20020 18079 1 A1	<input checked="" type="checkbox"/>	System and method for fast drawing of text fields and labels using a java swing application program interface	345/763
29	US 20020 18079 0 A1	<input checked="" type="checkbox"/>	System and method for encapsulating software components in an application program interface using a proxy object	345/762
30	US 20020 18078 7 A1	<input checked="" type="checkbox"/>	System and method for reducing memory use associated with the graphical representation of a list control	345/749
31	US 20020 18078 4 A1	<input checked="" type="checkbox"/>	Inheritance of background color in a containment hierarchy of objects in a graphical user interface	345/744
32	US 20020 18078 3 A1	<input checked="" type="checkbox"/>	Application program interface that can maintain similar look and feel of a displayed image regardless of whether the interface is platform dependent or platform independent	345/744
33	US 20020 17814 1 A1	<input checked="" type="checkbox"/>	Method and apparatus for remote inter-language method calling	707/1
34	US 20020 16584 8 A1	<input checked="" type="checkbox"/>	Method and structure for reducing search times	707/1

	Document ID	U	Title	Current OR
35	US 20020 16208 5 A1	<input checked="" type="checkbox"/>	PARALLELIZATION OF RESYNTHESIS	716/18
36	US 20020 14400 3 A1	<input checked="" type="checkbox"/>	Inter-thread communications between different components using double buffer	709/310
37	US 20020 13835 3 A1	<input checked="" type="checkbox"/>	Method and system for analysis of database records having fields with sets	705/26
38	US 20020 13373 8 A1	<input checked="" type="checkbox"/>	METHODS AND SYSTEMS FOR SAVING DATA POTENTIALLY MODIFIED BY A CRASHED COMPUTER PROGRAM EXECUTING IN A PREEMPTIVE MULTITASKING OPERATING SYSTEM ENVIRONMENT	714/6
39	US 20020 12917 3 A1	<input checked="" type="checkbox"/>	Communications system and method with non-blocking shared interface	709/310
40	US 20020 11660 0 A1	<input checked="" type="checkbox"/>	Method and apparatus for processing events in a multithreaded processor	712/218
41	US 20020 11222 7 A1	<input checked="" type="checkbox"/>	Dynamic compiler and method of compiling code to generate dominant path and to handle exceptions	717/148
42	US 20020 11189 1 A1	<input checked="" type="checkbox"/>	Accounting system for dynamic state of the portfolio reporting	705/36
43	US 20020 10810 7 A1	<input checked="" type="checkbox"/>	Hash table dispatch mechanism for interface methods	717/153
44	US 20020 10810 6 A1	<input checked="" type="checkbox"/>	Method and system of cache management using spatial separation of outliers	717/148
45	US 20020 10407 7 A1	<input checked="" type="checkbox"/>	Multi-threaded fragment patching	717/162
46	US 20020 09995 4 A1	<input checked="" type="checkbox"/>	Sensor for detecting and eliminating inter-process memory breaches in multitasking operating systems	713/200
47	US 20020 09561 4 A1	<input checked="" type="checkbox"/>	Method and apparatus for disabling a clock signal within a multithreaded processor	713/500
48	US 20020 09545 3 A1	<input checked="" type="checkbox"/>	Thread-specific heaps	709/107
49	US 20020 05294 7 A1	<input checked="" type="checkbox"/>	Method and system for managing performance of data transfers for a data access system	709/224
50	US 20020 04986 5 A1	<input checked="" type="checkbox"/>	Direct invocation of methods using class loader	709/315
51	US 20020 04971 9 A1	<input checked="" type="checkbox"/>	Application execution apparatus and method	707/1

	Document ID	U	Title	Current OR
52	US 20020 04280 7 A1	<input checked="" type="checkbox"/>	Low-contention grey object sets for concurrent, marking garbage collection	709/1
53	US 20020 04047 0 A1	<input checked="" type="checkbox"/>	Method and system of testing and verifying computer code in a multi-threaded environment	717/126
54	US 20020 03282 2 A1	<input checked="" type="checkbox"/>	Method and system for handling device driver interrupts	710/260
55	US 20020 03271 9 A1	<input checked="" type="checkbox"/>	Method and system of dynamic memory management	709/107
56	US 20020 02935 7 A1	<input checked="" type="checkbox"/>	Method and system of memory management using stack walking	714/9
57	US 20020 01687 8 A1	<input checked="" type="checkbox"/>	Technique for guaranteeing the availability of per thread storage in a distributed computing environment	710/200
58	US 20020 01070 3 A1	<input checked="" type="checkbox"/>	Methods and system for browsing large text files	707/101
59	US 20010 05645 7 A1	<input checked="" type="checkbox"/>	METHOD AND SYSTEM OF PROCESSING A PLURALITY OF DATA PROCESSING REQUESTS, AND METHOD AND SYSTEM OF EXECUTING A PROGRAM	709/104
60	US 20010 02746 2 A1	<input checked="" type="checkbox"/>	Automatic distributed processing system and computer program product	709/102
61	US 20010 01490 5 A1	<input checked="" type="checkbox"/>	Method and apparatus for managing a lock for an object	709/102
62	US 65670 84 B1	<input checked="" type="checkbox"/>	Lighting effect computation circuit and method therefore	345/426
63	US 65607 73 B1	<input checked="" type="checkbox"/>	Method and system for memory leak detection in an object-oriented environment during real-time trace processing	717/128
64	US 65465 48 B1	<input checked="" type="checkbox"/>	Method and system for compensating for output overhead in trace data using initial calibration information	717/128
65	US 65393 39 B1	<input checked="" type="checkbox"/>	Method and system for maintaining thread-relative metrics for trace data adjusted for thread switches	702/186
66	US 65359 05 B1	<input checked="" type="checkbox"/>	Method and apparatus for thread switching within a multithreaded processor	709/108
67	US 65257 47 B1	<input checked="" type="checkbox"/>	Method and system for conducting a discussion relating to an item	345/751
68	US 65163 45 B1	<input checked="" type="checkbox"/>	Approaches for determining actual physical topology of network based on gathered configuration information representing true neighboring devices	709/220
69	US 65104 98 B1	<input checked="" type="checkbox"/>	Method and apparatus for memory allocation in a multi-threaded virtual machine	711/153
70	US 65078 05 B1	<input checked="" type="checkbox"/>	Method and system for compensating for instrumentation overhead in trace data by detecting minimum event times	702/186

	Docum ent ID	U	Title	Current OR
71	US 64969 25 B1	<input checked="" type="checkbox"/>	Method and apparatus for processing an event occurrence within a multithreaded processor	712/244
72	US 64841 96 B1	<input checked="" type="checkbox"/>	Internet messaging system and method for use in computer networks	709/206
73	US 64704 87 B1	<input checked="" type="checkbox"/>	Parallelization of resynthesis	716/18
74	US 64634 80 B2	<input checked="" type="checkbox"/>	Method and system of processing a plurality of data processing requests, and method and system of executing a program	709/315
75	US 64571 42 B1	<input checked="" type="checkbox"/>	Method and apparatus for target application program supervision	714/38
76	US 64570 64 B1	<input checked="" type="checkbox"/>	Method and apparatus for detecting input directed to a thread in a multi-threaded process	709/318
77	US 64184 42 B1	<input checked="" type="checkbox"/>	Method and apparatus for providing thread-specific computer system parameters	707/100
78	US 63781 25 B1	<input checked="" type="checkbox"/>	Debugger thread identification points	717/129
79	US 63780 04 B1	<input checked="" type="checkbox"/>	Method of communicating asynchronous elements from a mini-port driver	709/321
80	US 63779 87 B1	<input checked="" type="checkbox"/>	Mechanism for determining actual physical topology of network based on gathered configuration information representing true neighboring devices	709/220
81	US 63779 84 B1	<input checked="" type="checkbox"/>	Web crawler system using parallel queues for queuing data sets having common address and concurrently downloading data associated with data set in each queue	709/217
82	US 63779 60 B1	<input checked="" type="checkbox"/>	Transactional configuration store and runtime versus administration isolation with version snapshots and aging	707/203
83	US 63634 11 B1	<input checked="" type="checkbox"/>	Intelligent network	709/202
84	US 63570 16 B1	<input checked="" type="checkbox"/>	Method and apparatus for disabling a clock signal within a multithreaded processor	713/601
85	US 63539 23 B1	<input checked="" type="checkbox"/>	Active debugging environment for debugging mixed-language scripting code	717/128
86	US 63494 06 B1	<input checked="" type="checkbox"/>	Method and system for compensating for instrumentation overhead in trace data by computing average minimum event times	717/128
87	US 63246 83 B1	<input checked="" type="checkbox"/>	System, method and program for debugging external programs in client/server-based relational database management systems	717/124
88	US 63212 65 B1	<input checked="" type="checkbox"/>	System and method for enforcing politeness while scheduling downloads in a web crawler	709/224
89	US 63145 63 B1	<input checked="" type="checkbox"/>	Expedited object locking and unlocking	717/108
90	US 62791 24 B1	<input checked="" type="checkbox"/>	Method and system for testing hardware and/or software applications	714/38
91	US 62790 30 B1	<input checked="" type="checkbox"/>	Dynamic JAVA.TM. class selection and download based on changeable attributes	709/203
92	US 62758 43 B1	<input checked="" type="checkbox"/>	Method and apparatus for processing multiple service requests within a global transaction by a single server application program instance	709/101
93	US 62725 18 B1	<input checked="" type="checkbox"/>	System and method for porting a multithreaded program to a job model	709/102

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94	US 62694 25 B1	<input checked="" type="checkbox"/>	Accessing data from a multiple entry fully associative cache buffer in a multithread data processing system	711/133
95	US 62693 91 B1	<input checked="" type="checkbox"/>	Multi-processor scheduling kernel	709/100
96	US 62600 75 B1	<input checked="" type="checkbox"/>	System and method for providing shared global offset table for common shared library in a computer system	709/310
97	US 62470 25 B1	<input checked="" type="checkbox"/>	Locking and unlocking mechanism for controlling concurrent access to objects	707/206
98	US 62335 99 B1	<input checked="" type="checkbox"/>	Apparatus and method for retrofitting multi-threaded operations on a computer by partitioning and overlapping registers	709/102
99	US 62090 66 B1	<input checked="" type="checkbox"/>	Method and apparatus for memory allocation in a multi-threaded virtual machine	711/153
100	US 61821 83 B1	<input checked="" type="checkbox"/>	Communications system and method with multilevel connection identification	710/305
101	US 61734 42 B1	<input checked="" type="checkbox"/>	Busy-wait-free synchronization	717/141
102	US 61580 24 A	<input checked="" type="checkbox"/>	Method and apparatus for structured memory analysis of data processing systems and applications	714/37
103	US 60584 60 A	<input checked="" type="checkbox"/>	Memory allocation in a multithreaded environment	711/153
104	US 60583 93 A	<input checked="" type="checkbox"/>	Dynamic connection to a remote tool in a distributed processing system environment used for debugging	707/10
105	US 60263 96 A	<input checked="" type="checkbox"/>	Knowledge-based moderator for electronic mail help lists	707/4
106	US 60028 72 A	<input checked="" type="checkbox"/>	Method and apparatus for structured profiling of data processing systems and applications	717/127
107	US 60028 52 A	<input checked="" type="checkbox"/>	Method and system for confirming receipt of data opportunistically broadcast to client computer systems	709/203
108	US 59917 92 A	<input checked="" type="checkbox"/>	Method, apparatus and computer program product for dynamically managing a thread pool of reusable threads in a computer system	709/102
109	US 59908 52 A	<input checked="" type="checkbox"/>	Display screen duplication system and method	345/2.1
110	US 59387 61 A	<input checked="" type="checkbox"/>	Method and apparatus for branch target prediction	712/238
111	US 59096 79 A	<input checked="" type="checkbox"/>	Knowledge-based moderator for electronic mail help lists	707/4
112	US 58931 17 A	<input checked="" type="checkbox"/>	Time-stamped database transaction and version management system	707/203
113	US 58929 44 A	<input checked="" type="checkbox"/>	Program execution and operation right management system suitable for single virtual memory scheme	709/100
114	US 58812 69 A	<input checked="" type="checkbox"/>	Simulation of multiple local area network clients on a single workstation	703/21
115	US 58128 52 A	<input checked="" type="checkbox"/>	Software implemented method for thread-privatizing user-specified global storage objects in parallel computer programs via program transformation	717/149
116	US 58101 46 A	<input checked="" type="checkbox"/>	Wide edge lead currency thread detection system	194/206



	Docum ent ID	U	Title	Current OR
117	US 57939 73 A	<input checked="" type="checkbox"/>	Method and system for opportunistic broadcasting of data	709/223
118	US 56320 32 A	<input checked="" type="checkbox"/>	Cross address space thread control in a multithreaded environment	709/100
119	US 55903 35 A	<input checked="" type="checkbox"/>	Process for analysis of deadlocks in an operating system	709/104
120	US 55577 47 A	<input checked="" type="checkbox"/>	Network policy implementation system for performing network control operations in response to changes in network state	709/223
121	US 55353 86 A	<input checked="" type="checkbox"/>	Transaction and version management system	707/203
122	US 55353 61 A	<input checked="" type="checkbox"/>	Cache block replacement scheme based on directory control bit set/reset and hit/miss basis in a multiheading multiprocessor environment	711/145
123	US 52610 97 A	<input checked="" type="checkbox"/>	Computer system and method for executing command scripts using multiple synchronized threads	709/106
124	US 45273 83 A	<input checked="" type="checkbox"/>	Threads for identification of garments	57/200
125	US 44457 94 A	<input checked="" type="checkbox"/>	Self-locking threaded bearing and bearing assembly and method of making such assembly	384/428
126	US 39124 11 A	<input type="checkbox"/>	Thread latching mechanism	403/259

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1	JP 20011 88685 A	<input type="checkbox"/>	METHOD AND DEVICE FOR MANAGING LOCK OF OBJECT	
2	JP 20002 59432 A	<input checked="" type="checkbox"/>	FILE WRITING SYSTEM FOR MULTI-THREAD PROGRAM	
3	JP 06223 201 A	<input checked="" type="checkbox"/>	PARALLEL IMAGE GENERATING DEVICE	
4	JP 06131 393 A	<input checked="" type="checkbox"/>	ASSOCIATION STORAGE DEVICE	
5	NNRD4 47124	<input checked="" type="checkbox"/>	Thread specific storage across platform Unix/NT/OS2/Linux ...	
6	NN961 2167	<input checked="" type="checkbox"/>	Asynchronous Posix Threads	
7	NN940 8111	<input checked="" type="checkbox"/>	Multiple Threads of Execution in REXX Programs	
8	NN921 1182	<input checked="" type="checkbox"/>	Freeing Thread Stacks in OS/2 using the ALPs Begin and End Thread APIs.	
9	US 20020 10384 7 A	<input checked="" type="checkbox"/>	Inter-thread data communication system for digital communication, indicates usage of one thread's data by another thread, by setting flag corresponding to used thread in attention register of using thread	
10	US 64184 42 B	<input checked="" type="checkbox"/>	Thread specific system parameter searching method for computer system, involves retrieving parameter from thread database based on thread identifiers and independent of existing thread specific data structures	
11	US 20020 07812 4 A	<input checked="" type="checkbox"/>	Thread scheduling method in multi-processor computer system, involves identifying and scheduling threads having same id by mining patterns in entries for contiguous cache lines	
12	CN 13429 40 A	<input checked="" type="checkbox"/>	Integrated processor has protocol processor unit that switches priority of execution of threads, based on identification of events having long and short waiting time, in response to operation instruction	
13	US 62984 11 B	<input checked="" type="checkbox"/>	Data accessing method for cache in multithreaded system involves comparing physical address corresponding to virtual address with physical address of data in cache and if they match setting thread indicator and accessing data in cache	
14	EP 10319 27 A	<input checked="" type="checkbox"/>	Data processing method for multi-threaded applications by identifying available memory blocks in response to allocation request based on permitted access to shared memory	
15	JP 20001 09271 A	<input type="checkbox"/>	Thread roll processing apparatus for e.g. synthetic fiber, has measurement unit for thread form or thread roll	

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1	L1	933	thread adj2 (id identif\$6)	USPAT; US-PGPUB
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3	L3	260	1 and 2	USPAT; US-PGPUB
4	L4	6827	context near5 (switch\$3 chang\$3)	USPAT; US-PGPUB
5	L6	140	thread adj2 (id identif\$6)	EPO; JPO; DERWENT; IBM_TDB
6	L7	105241	(data load read miss) near10 (delay latency wait\$3)	USPAT; EPO; JPO; DERWENT; IBM_TDB
7	L8	642	context near5 (switch\$3 chang\$3)	EPO; JPO; DERWENT; IBM_TDB
8	L12	176	3 not (5 11)	USPAT; US-PGPUB
9	L5	84	4 and 3	USPAT; US-PGPUB
10	L11	66	1 and 4 not 5	USPAT; US-PGPUB
11	L9	7	6 and 8	EPO; JPO; DERWENT; IBM_TDB
12	L10	4	6 and 7	EPO; JPO; DERWENT; IBM_TDB
13	L13	141	(result return\$3 respond\$3 retriev\$3) near20 1	USPAT; US-PGPUB
14	L15	126	13 not (9 11)	USPAT; US-PGPUB
15	L14	15	(result return\$3 respond\$3 retriev\$3) near20 6	EPO; JPO; DERWENT; IBM_TDB

	Docum ent ID	U	Title	Current OR
1	US 20030 11038 2 A1	<input type="checkbox"/>	Processing data	713/172
2	US 20030 10560 9 A1	<input checked="" type="checkbox"/>	System and method for generating waveforms using waveform segment queues	702/124
3	US 20030 10560 7 A1	<input checked="" type="checkbox"/>	Circuit testing with ring-connected test instrument modules	702/121
4	US 20030 09736 0 A1	<input checked="" type="checkbox"/>	Object locking in a shared VM environment	707/8
5	US 20030 08426 9 A1	<input checked="" type="checkbox"/>	Method and apparatus for communicating between processing entities in a multi-processor	712/36
6	US 20030 07893 0 A1	<input checked="" type="checkbox"/>	File and content management	707/10
7	US 20030 06129 2 A1	<input checked="" type="checkbox"/>	System and method for providing an interface for com-compliant applications to communicate with embedded systems	709/206
8	US 20030 06125 8 A1	<input checked="" type="checkbox"/>	Method and apparatus for processing an event occurrence for at least one thread within a multithreaded processor.	709/102
9	US 20030 05620 0 A1	<input checked="" type="checkbox"/>	Runtime monitoring in component-based systems	717/128
10	US 20030 05619 9 A1	<input checked="" type="checkbox"/>	Hyperbolic tree space display of computer system monitoring and analysis data	717/127
11	US 20030 05605 9 A1	<input checked="" type="checkbox"/>	Optimizing data transfer performance through partial write command purging in a disc drive	711/112
12	US 20030 05602 0 A1	<input checked="" type="checkbox"/>	Hardware message buffer for supporting inter-processor communication	709/313
13	US 20030 05589 0 A1	<input checked="" type="checkbox"/>	Information processing system	709/203
14	US 20030 04117 3 A1	<input checked="" type="checkbox"/>	Synchronization objects for multi-computer systems	709/248
15	US 20030 03720 3 A1	<input checked="" type="checkbox"/>	Multi-threaded random access storage device qualification tool	711/104
16	US 20030 03351 1 A1	<input checked="" type="checkbox"/>	Processor having multiple program counters and trace buffers outside an execution pipeline	712/235
17	US 20030 01868 4 A1	<input checked="" type="checkbox"/>	Multi-thread execution method and parallel processor system	709/102

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18	US 20030 01621 7 A1	<input checked="" type="checkbox"/>	Method and apparatus for processing non-planar video graphics primitives	345/423
19	US 20030 01159 5 A1	<input checked="" type="checkbox"/>	Apparatus for processing non-planar video graphics primitives and associated method of operation	345/423
20	US 20030 00964 8 A1	<input checked="" type="checkbox"/>	Apparatus for supporting a logically partitioned computer system	711/202
21	US 20030 00526 6 A1	<input checked="" type="checkbox"/>	Multithreaded processor capable of implicit multithreaded execution of a single-thread program	712/220
22	US 20030 00526 3 A1	<input checked="" type="checkbox"/>	Shared resource queue for simultaneous multithreaded processing	712/218
23	US 20020 19904 5 A1	<input checked="" type="checkbox"/>	Apparatus, method and computer program product for converting simple locks in a multiprocessor system	710/100
24	US 20020 19892 2 A1	<input checked="" type="checkbox"/>	System and method for providing an interface for scripting programs to communicate with embedded systems	709/101
25	US 20020 19889 3 A1	<input checked="" type="checkbox"/>	System and method for formatting data for transmission between an embedded computer and a host computer having different machine characteristics	707/102
26	US 20020 19888 7 A1	<input checked="" type="checkbox"/>	System and method for building a database defining a plurality of communication interfaces	707/100
27	US 20020 19867 5 A1	<input checked="" type="checkbox"/>	System and method for generating data sets for testing embedded systems	702/122
28	US 20020 19445 7 A1	<input checked="" type="checkbox"/>	Memory system for ordering load and store instructions in a processor that performs out-of-order multithread execution	712/218
29	US 20020 18859 5 A1	<input checked="" type="checkbox"/>	System and method for automatically generating code templates for communication via a predefined communication interface	707/1
30	US 20020 18399 7 A1	<input checked="" type="checkbox"/>	Apparatus and method for specifying the configuration of mixed-language simulation models	703/13
31	US 20020 17003 7 A1	<input checked="" type="checkbox"/>	Apparatus and method for controlling event ordering in a mixed- language simulator	717/131
32	US 20020 16598 8 A1	<input checked="" type="checkbox"/>	System, method, and article of manufacture for wireless enablement of the world wide web using a wireless gateway	709/246
33	US 20020 16584 8 A1	<input checked="" type="checkbox"/>	Method and structure for reducing search times	707/1
34	US 20020 15678 6 A1	<input checked="" type="checkbox"/>	Asynchronous database updates	707/10

	Docum ent ID	U	Title	Current OR
35	US 20020 13835 3 A1	<input checked="" type="checkbox"/>	Method and system for analysis of database records having fields with sets	705/26
36	US 20020 13358 6 A1	<input checked="" type="checkbox"/>	Method and device for monitoring data traffic and preventing unauthorized access to a network	709/224
37	US 20020 12933 9 A1	<input checked="" type="checkbox"/>	Parallelism performance analysis based on execution trace information	717/127
38	US 20020 12917 3 A1	<input checked="" type="checkbox"/>	Communications system and method with non-blocking shared interface	709/310
39	US 20020 12408 5 A1	<input checked="" type="checkbox"/>	Method of simulating operation of logical unit, and computer-readable recording medium retaining program for simulating operation of logical unit	709/226
40	US 20020 12082 8 A1	<input checked="" type="checkbox"/>	Bit field manipulation	712/200
41	US 20020 12079 8 A1	<input checked="" type="checkbox"/>	Global access bus architecture	710/107
42	US 20020 12066 5 A1	<input checked="" type="checkbox"/>	Portable thread environment	709/107
43	US 20020 11660 0 A1	<input checked="" type="checkbox"/>	Method and apparatus for processing events in a multithreaded processor	712/218
44	US 20020 11658 7 A1	<input checked="" type="checkbox"/>	External memory engine selectable pipeline architecture	711/154
45	US 20020 11644 9 A1	<input checked="" type="checkbox"/>	Load-shift carry instruction	709/203
46	US 20020 11644 2 A1	<input checked="" type="checkbox"/>	Route switch packet architecture	709/106
47	US 20020 11222 7 A1	<input checked="" type="checkbox"/>	Dynamic compiler and method of compiling code to generate dominant path and to handle exceptions	717/148
48	US 20020 11200 8 A1	<input checked="" type="checkbox"/>	Electronic mail system with methodology providing distributed message store	709/206
49	US 20020 10810 7 A1	<input checked="" type="checkbox"/>	Hash table dispatch mechanism for interface methods	717/153
50	US 20020 10810 6 A1	<input checked="" type="checkbox"/>	Method and system of cache management using spatial separation of outliers	717/148
51	US 20020 10795 3 A1	<input checked="" type="checkbox"/>	Method and device for monitoring data traffic and preventing unauthorized access to a network	709/224

	Docum ent ID	U	Title	Current OR
52	US 20020 10407 7 A1	<input checked="" type="checkbox"/>	Multi-threaded fragment patching	717/162
53	US 20020 09561 4 A1	<input checked="" type="checkbox"/>	Method and apparatus for disabling a clock signal within a multithreaded processor	713/500
54	US 20020 09199 7 A1	<input checked="" type="checkbox"/>	Diagnostic tool for a portable thread environment	717/127
55	US 20020 09191 5 A1	<input checked="" type="checkbox"/>	Load prediction and thread identification in a multithreaded microprocessor	712/225
56	US 20020 08329 7 A1	<input checked="" type="checkbox"/>	Multi-thread packet processor	712/18
57	US 20020 07812 1 A1	<input checked="" type="checkbox"/>	Real-time scheduler	709/102
58	US 20020 07811 9 A1	<input checked="" type="checkbox"/>	System and method for improved complex storage locks	709/102
59	US 20020 07308 2 A1	<input checked="" type="checkbox"/>	System modification processing technique implemented on an information storage and retrieval system	707/3
60	US 20020 07295 0 A1	<input checked="" type="checkbox"/>	Item allocation method, system and program for workflow system	705/9
61	US 20020 07294 9 A1	<input checked="" type="checkbox"/>	Item allocation method, system and program for workflow system	705/9
62	US 20020 06595 3 A1	<input checked="" type="checkbox"/>	Priority in a portable thread environment	709/328
63	US 20020 05292 6 A1	<input checked="" type="checkbox"/>	Thread suspension system and method using trapping instructions	709/217
64	US 20020 05291 1 A1	<input checked="" type="checkbox"/>	Finite state machine in a portable thread environment	709/107
65	US 20020 04986 5 A1	<input checked="" type="checkbox"/>	Direct invocation of methods using class loader	709/315
66	US 20020 04623 1 A1	<input checked="" type="checkbox"/>	Distributed applications in a portable thread environment	709/107
67	US 20020 04280 7 A1	<input checked="" type="checkbox"/>	Low-contention grey object sets for concurrent, marking garbage collection	709/1
68	US 20020 04152 0 A1	<input checked="" type="checkbox"/>	Scratchpad memory	365/189 .01

	Docum ent ID	U	Title	Current OR
69	US 20020 04047 0 A1	<input checked="" type="checkbox"/>	Method and system of testing and verifying computer code in a multi-threaded environment	717/126
70	US 20020 03282 2 A1	<input checked="" type="checkbox"/>	Method and system for handling device driver interrupts	710/260
71	US 20020 03271 9 A1	<input checked="" type="checkbox"/>	Method and system of dynamic memory management	709/107
72	US 20020 02935 7 A1	<input checked="" type="checkbox"/>	Method and system of memory management using stack walking	714/9
73	US 20020 01988 1 A1	<input checked="" type="checkbox"/>	System, method and computer program product for habitat-based universal application of functions to network data	709/246
74	US 20010 05103 2 A1	<input checked="" type="checkbox"/>	Delayed decision recording device	386/46
75	US 20010 04977 4 A1	<input checked="" type="checkbox"/>	APPARATUS SYSTEM AND METHOD FOR N-WAY RAID CONTROLLER HAVING IMPROVED PERFORMANCE AND FAULT TOLERANCE	711/148
76	US 20010 04209 8 A1	<input checked="" type="checkbox"/>	FACILITATING ANNOTATION CREATION AND NOTIFICATION VIA ELECTRONIC MAIL	709/206
77	US 20010 01494 1 A1	<input checked="" type="checkbox"/>	Processor having multiple program counters and trace buffers outside an execution pipeline	712/228
78	US 65775 42 B2	<input checked="" type="checkbox"/>	Scratchpad memory	365/189 .01
79	US 65670 84 B1	<input checked="" type="checkbox"/>	Lighting effect computation circuit and method therefore	345/426
80	US 65606 28 B1	<input checked="" type="checkbox"/>	Apparatus, method, and recording medium for scheduling execution using time slot data	709/103
81	US 64969 25 B1	<input checked="" type="checkbox"/>	Method and apparatus for processing an event occurrence within a multithreaded processor	712/244
82	US 64938 20 B2	<input checked="" type="checkbox"/>	Processor having multiple program counters and trace buffers outside an execution pipeline	712/235
83	US 64933 15 B1	<input checked="" type="checkbox"/>	ATM switch for routing different cell types	370/229
84	US 64808 18 B1	<input checked="" type="checkbox"/>	Debugging techniques in a multithreaded environment	703/26
85	US 64703 76 B1	<input checked="" type="checkbox"/>	Processor capable of efficiently executing many asynchronous event tasks	709/108
86	US 64668 98 B1	<input checked="" type="checkbox"/>	Multithreaded, mixed hardware description languages logic simulation on engineering workstations	703/17
87	US 64635 22 B1	<input checked="" type="checkbox"/>	Memory system for ordering load and store instructions in a processor that performs multithread execution	712/216
88	US 64630 72 B1	<input checked="" type="checkbox"/>	Method and apparatus for sharing access to a bus	370/439



	Docum ent ID	U	Title	Current OR
89	US 64425 85 B1	<input checked="" type="checkbox"/>	Method for scheduling contexts based on statistics of memory system interactions in a computer system	709/108
90	US 64386 71 B1	<input checked="" type="checkbox"/>	Generating partition corresponding real address in partitioned mode supporting system	711/173
91	US 64385 75 B1	<input checked="" type="checkbox"/>	System, method, and article of manufacture for wireless enablement of the world wide web using a wireless gateway	709/200
92	US 64336 03 B1	<input checked="" type="checkbox"/>	Pulse-based high speed flop circuit	327/211
93	US 64119 88 B1	<input checked="" type="checkbox"/>	Method and system for presentation conferencing	709/204
94	US 63780 04 B1	<input checked="" type="checkbox"/>	Method of communicating asynchronous elements from a mini-port driver	709/321
95	US 63570 16 B1	<input checked="" type="checkbox"/>	Method and apparatus for disabling a clock signal within a multithreaded processor	713/601
96	US 63538 29 B1	<input checked="" type="checkbox"/>	Method and system for memory allocation in a multiprocessing environment	707/100
97	US 63453 13 B1	<input checked="" type="checkbox"/>	Recovery of synchronization constructs	709/315
98	US 63321 78 B1	<input checked="" type="checkbox"/>	Method for estimating statistics of properties of memory system transactions	711/118
99	US 63212 65 B1	<input checked="" type="checkbox"/>	System and method for enforcing politeness while scheduling downloads in a web crawler	709/224
100	US 63145 63 B1	<input checked="" type="checkbox"/>	Expedited object locking and unlocking	717/108
101	US 63083 19 B1	<input checked="" type="checkbox"/>	Thread suspension system and method using trapping instructions in delay slots	717/141
102	US 63081 82 B1	<input checked="" type="checkbox"/>	Information processing apparatus	707/103 R
103	US 63077 89 B1	<input checked="" type="checkbox"/>	Scratchpad memory	365/189 .01
104	US 62790 30 B1	<input checked="" type="checkbox"/>	Dynamic JAVA.TM. class selection and download based on changeable attributes	709/203
105	US 62694 25 B1	<input checked="" type="checkbox"/>	Accessing data from a multiple entry fully associative cache buffer in a multithread data processing system	711/133
106	US 62631 47 B1	<input checked="" type="checkbox"/>	Delayed decision recording device	386/46
107	US 62567 04 B1	<input checked="" type="checkbox"/>	Task management for data accesses to multiple logical partitions on physical disk drives in computer systems	711/112
108	US 62471 21 B1	<input checked="" type="checkbox"/>	Multithreading processor with thread predictor	712/239
109	US 62470 25 B1	<input checked="" type="checkbox"/>	Locking and unlocking mechanism for controlling concurrent access to objects	707/206
110	US 62405 09 B1	<input checked="" type="checkbox"/>	Out-of-pipeline trace buffer for holding instructions that may be re-executed following misspeculation	712/228
111	US 62405 02 B1	<input checked="" type="checkbox"/>	Apparatus for dynamically reconfiguring a processor	712/15

	Docum ent ID	U	Title	Current OR
112	US 62370 59 B1	<input checked="" type="checkbox"/>	Method for estimating statistics of properties of memory system interactions among contexts in a computer system	711/100
113	US 62370 43 B1	<input checked="" type="checkbox"/>	System and method for adding highly optimized synchronization capability to objects at run-time	709/316
114	US 62335 99 B1	<input checked="" type="checkbox"/>	Apparatus and method for retrofitting multi-threaded operations on a computer by partitioning and overlapping registers	709/102
115	US 62333 61 B1	<input checked="" type="checkbox"/>	Topography processor system	382/260
116	US 62303 13 B1	<input checked="" type="checkbox"/>	Parallelism performance analysis based on execution trace information	717/128
117	US 62297 89 B1	<input checked="" type="checkbox"/>	Congestion avoidance in an ATM switch	370/235
118	US 62125 42 B1	<input checked="" type="checkbox"/>	Method and system for executing a program within a multiscalar processor by processing linked thread descriptors	709/102
119	US 62021 27 B1	<input checked="" type="checkbox"/>	Apparatus for spatial and temporal sampling in a computer memory system	711/118
120	US 61822 10 B1	<input checked="" type="checkbox"/>	Processor having multiple program counters and trace buffers outside an execution pipeline	712/235
121	US 61821 83 B1	<input checked="" type="checkbox"/>	Communications system and method with multilevel connection identification	710/305
122	US 61734 42 B1	<input checked="" type="checkbox"/>	Busy-wait-free synchronization	717/141
123	US 61667 34 A	<input checked="" type="checkbox"/>	Portable interactive graphics display tablet and communications system	345/748
124	US 61484 17 A	<input checked="" type="checkbox"/>	Method for determining a source of failure during a file system access	714/25
125	US 61446 40 A	<input checked="" type="checkbox"/>	ATM switch	370/236
126	US 61286 40 A	<input checked="" type="checkbox"/>	Method and apparatus for user-level support for multiple event synchronization	709/102
127	US 61283 06 A	<input checked="" type="checkbox"/>	Cell queue formation in an ATM switch	370/412
128	US 61227 12 A	<input checked="" type="checkbox"/>	Cache coherency controller of cache memory for maintaining data anti-dependence when threads are executed in parallel	711/141
129	US 61118 94 A	<input checked="" type="checkbox"/>	Hardware interface between a switch adapter and a communications subsystem in a data processing system	370/469
130	US 61087 54 A	<input checked="" type="checkbox"/>	Thread-local synchronization construct cache	711/122
131	US 61050 53 A	<input checked="" type="checkbox"/>	Operating system for a non-uniform memory access multiprocessor system	709/105
132	US 60921 54 A	<input checked="" type="checkbox"/>	Method of pre-caching or pre-fetching data utilizing thread lists and multimedia editing systems using such pre-caching	711/137
133	US 60845 84 A	<input checked="" type="checkbox"/>	Computer system supporting portable interactive graphics display tablet and communications systems	345/864
134	US 60790 08 A	<input checked="" type="checkbox"/>	Multiple thread multiple data predictive coded parallel processing system and method	712/11

	Docum ent ID	U	Title	Current OR
135	US 60584 66 A	<input checked="" type="checkbox"/>	System for allocation of execution resources amongst multiple executing processes	712/15
136	US 60527 31 A	<input checked="" type="checkbox"/>	Apparatus, method and computer program for providing arbitrary locking requesters for controlling concurrent access to server resources	709/229
137	US 60521 20 A	<input checked="" type="checkbox"/>	Method of operating a portable interactive graphics display tablet and communications systems	345/700
138	US 60353 74 A	<input checked="" type="checkbox"/>	Method of executing coded instructions in a multiprocessor having shared execution resources including active, nap, and sleep states in accordance with cache miss latency	711/118
139	US 60211 15 A	<input checked="" type="checkbox"/>	ATM switch flow control	370/235
140	US 59908 52 A	<input checked="" type="checkbox"/>	Display screen duplication system and method	345/2.1
141	US 59870 08 A	<input checked="" type="checkbox"/>	ATM switch	370/236
142	US 59616 39 A	<input checked="" type="checkbox"/>	Processor and method for dynamically inserting auxiliary instructions within an instruction stream during execution	712/242
143	US 59464 87 A	<input checked="" type="checkbox"/>	Object-oriented multi-media architecture	717/148
144	US 59206 89 A	<input checked="" type="checkbox"/>	System and method for low overhead, high precision performance measurements using state transitions	714/47
145	US 59139 25 A	<input checked="" type="checkbox"/>	Method and system for constructing a program including out-of-order threads and processor and method for executing threads out-of-order	712/206
146	US 58900 08 A	<input checked="" type="checkbox"/>	Method for dynamically reconfiguring a processor	712/15
147	US 58871 66 A	<input checked="" type="checkbox"/>	Method and system for constructing a program including a navigation instruction	709/102
148	US 58729 13 A	<input checked="" type="checkbox"/>	System and method for low overhead, high precision performance measurements using state transistions	714/47
149	US 58677 36 A	<input checked="" type="checkbox"/>	Methods for simplified integration of host-based storage array control functions using read and write operations on a storage array control port	710/74
150	US 58527 31 A	<input checked="" type="checkbox"/>	Computer program product for synchronizing static variable initialization and reference under a multi-threaded computer environment	709/100
151	US 58357 63 A	<input checked="" type="checkbox"/>	Threaded environment for computer systems without native threading support	709/101
152	US 58225 25 A	<input checked="" type="checkbox"/>	Method and system for presentation conferencing	709/204
153	US 58101 46 A	<input checked="" type="checkbox"/>	Wide edge lead currency thread detection system	194/206
154	US 57713 82 A	<input checked="" type="checkbox"/>	System and method for synchronizing static variable initialization and reference under a multi-threaded computer environment	709/100
155	US 57457 78 A	<input checked="" type="checkbox"/>	Apparatus and method for improved CPU affinity in a multiprocessor system	712/1
156	US 56757 96 A	<input checked="" type="checkbox"/>	Concurrency management component for use by a computer program during the transfer of a message	709/100
157	US 56447 68 A	<input checked="" type="checkbox"/>	Systems and methods for sharing resources in a multi-user environment	709/102

	Docum ent ID	U	Title	Current OR
158	US 56301 28 A	<input checked="" type="checkbox"/>	Controlled scheduling of program threads in a multitasking operating system	709/103
159	US 56258 21 A	<input checked="" type="checkbox"/>	Asynchronous or synchronous operation of event signaller by event management services in a computer system	709/100
160	US 55985 62 A	<input checked="" type="checkbox"/>	System and method for adding new waitable object types to object oriented computer operating system	709/104
161	US 55749 39 A	<input checked="" type="checkbox"/>	Multiprocessor coupling system with integrated compile and run time scheduling for parallelism	712/24
162	US 55600 29 A	<input checked="" type="checkbox"/>	Data processing system with synchronization coprocessor for multiple threads	712/25
163	US 55308 68 A	<input checked="" type="checkbox"/>	Notification of event handlers in broadcast or propagation mode by event management services in a computer system	709/318
164	US 55242 47 A	<input checked="" type="checkbox"/>	System for scheduling programming units to a resource based on status variables indicating a lock or lock-wait state thereof	710/200
165	US 55048 98 A	<input checked="" type="checkbox"/>	Threaded environment for AS/400	709/101
166	US 54308 50 A	<input checked="" type="checkbox"/>	Data processing system with synchronization coprocessor for multiple threads	709/314
167	US 53554 84 A	<input checked="" type="checkbox"/>	Dynamically established event monitors in event management services of a computer system	717/127
168	US 53534 18 A	<input checked="" type="checkbox"/>	System storing thread descriptor identifying one of plural threads of computation in storage only when all data for operating on thread is ready and independently of resultant imperative processing of thread	709/108
169	US 53455 88 A	<input checked="" type="checkbox"/>	Thread private memory storage of multi-thread digital data processors using access descriptors for uniquely identifying copies of data created on an as-needed basis	709/107
170	US 53054 54 A	<input checked="" type="checkbox"/>	Notification of event handlers in broadcast or propagation mode by event management services in a computer system	709/318
171	US 52971 37 A	<input checked="" type="checkbox"/>	Process for routing data packets around a multi-node communications network	370/403
172	US 52476 75 A	<input checked="" type="checkbox"/>	Preemptive and non-preemptive scheduling and execution of program threads in a multitasking operating system	709/103
173	US 52376 84 A	<input checked="" type="checkbox"/>	Customized and versatile event monitor within event management services of a computer system	709/318
174	US 52010 49 A	<input checked="" type="checkbox"/>	System for executing applications program concurrently/serially on different virtual machines	709/1
175	US 50635 00 A	<input checked="" type="checkbox"/>	System for executing segments of application program concurrently/serially on different/same virtual machine	709/226
176	US 46632 20 A	<input type="checkbox"/>	Polyolefin-containing extrudable compositions and methods for their formation into elastomeric products including microfibers	428/221

	Docum ent ID	U	Title	Current OR
1	US 20030 11494 9 A1	<input type="checkbox"/>	Dynamic diagnostic program for determining thread wait time	700/107
2	US 20030 10562 0 A1	<input checked="" type="checkbox"/>	System, method and article of manufacture for interface constructs in a programming language capable of programming hardware architectures	703/22
3	US 20030 09379 2 A1	<input checked="" type="checkbox"/>	Method and apparatus for delivery of television programs and targeted de-coupled advertising	725/46
4	US 20030 08861 0 A1	<input checked="" type="checkbox"/>	Multi-core multi-thread processor	709/107
5	US 20030 07417 7 A1	<input checked="" type="checkbox"/>	System, method and article of manufacture for a simulator plug-in for co-simulation purposes	703/22
6	US 20030 06791 3 A1	<input checked="" type="checkbox"/>	Programmable storage network protocol handler architecture	370/389
7	US 20030 06755 4 A1	<input checked="" type="checkbox"/>	System and method for personalized TV	348/461
8	US 20030 04667 1 A1	<input checked="" type="checkbox"/>	System, method and article of manufacture for signal constructs in a programming language capable of programming hardware architectures	717/141
9	US 20030 04666 8 A1	<input checked="" type="checkbox"/>	System, method and article of manufacture for distributing IP cores	717/131
10	US 20030 03732 1 A1	<input checked="" type="checkbox"/>	System, method and article of manufacture for extensions in a programming language capable of programming hardware architectures	717/149
11	US 20030 03722 8 A1	<input checked="" type="checkbox"/>	System and method for instruction level multithreading scheduling in a embedded processor	712/245
12	US 20030 03359 4 A1	<input checked="" type="checkbox"/>	System, method and article of manufacture for parameterized expression libraries	717/141
13	US 20030 03358 8 A1	<input checked="" type="checkbox"/>	System, method and article of manufacture for using a library map to create and maintain IP cores effectively	717/107
14	US 20030 02886 4 A1	<input checked="" type="checkbox"/>	System, method and article of manufacture for successive compilations using incomplete parameters	717/141
15	US 20030 02383 5 A1	<input checked="" type="checkbox"/>	Method and system to perform a thread switching operation within a multithreaded processor based on dispatch of a quantity of instruction information for a full instruction	712/214
16	US 20030 02383 4 A1	<input checked="" type="checkbox"/>	Method and system to insert a flow marker into an instruction stream to indicate a thread switching operation within a multithreaded processor	712/214
17	US 20030 02365 9 A1	<input checked="" type="checkbox"/>	Method and apparatus for thread switching within a multithreaded processor	709/102

	Docum ent ID	U	Title	Current OR
18	US 20030 02365 8 A1	<input checked="" type="checkbox"/>	Method and system to perform a thread switching operation within a multithreaded processor based on detection of the absence of a flow of instruction information for a thread	709/102
19	US 20030 01868 7 A1	<input checked="" type="checkbox"/>	Method and system to perform a thread switching operation within a multithreaded processor based on detection of a flow marker within an instruction information	709/102
20	US 20030 01868 6 A1	<input checked="" type="checkbox"/>	Method and system to perform a thread switching operation within a multithreaded processor based on detection of a stall condition	709/102
21	US 20030 01868 5 A1	<input checked="" type="checkbox"/>	Method and system to perform a thread switching operation within a multithreaded processor based on detection of a branch instruction	709/102
22	US 20030 01461 2 A1	<input checked="" type="checkbox"/>	MULTI-THREADED PROCESSOR BY MULTIPLE-BIT FLIP-FLOP GLOBAL SUBSTITUTION	712/215
23	US 20030 00468 3 A1	<input checked="" type="checkbox"/>	Instruction pre-fetching mechanism for a multithreaded program execution	702/186
24	US 20020 19917 3 A1	<input checked="" type="checkbox"/>	System, method and article of manufacture for a debugger capable of operating across multiple threads and lock domains	717/129
25	US 20020 18883 1 A1	<input checked="" type="checkbox"/>	Annotations for transaction tracing	712/227
26	US 20020 16611 0 A1	<input checked="" type="checkbox"/>	Apparatus and method for performing event processing in a mixed-language simulator	717/106
27	US 20020 13871 7 A1	<input checked="" type="checkbox"/>	Multiple-thread processor with single-thread interface shared among threads	712/235
28	US 20020 11192 9 A1	<input checked="" type="checkbox"/>	Concurrent data recall in a hierarchical storage environment using plural queues	707/1
29	US 20020 10800 3 A1	<input checked="" type="checkbox"/>	COMMAND QUEUEING ENGINE	710/39
30	US 20020 10399 0 A1	<input checked="" type="checkbox"/>	Programmed load precession machine	712/215
31	US 20020 08773 6 A1	<input checked="" type="checkbox"/>	Parallel dispatch wait signaling method, method for reducing contention of highly contended dispatcher lock, and related operating systems, multiprocessor computer systems and products	709/312
32	US 20020 07812 2 A1	<input checked="" type="checkbox"/>	Switching method in a multi-threaded processor	709/102
33	US 20020 01386 6 A1	<input checked="" type="checkbox"/>	RETRIEVAL CHANNELS FOR A DATA CONTROLLER	710/22
34	US 20020 00266 7 A1	<input checked="" type="checkbox"/>	System and method for instruction level multithreading in an embedded processor using zero-time context switching	712/228

	Docum ent ID	U	Title	Current OR
35	US 20010 04746 8 A1	<input checked="" type="checkbox"/>	Branch and return on blocked load or store	712/228
36	US 20010 01490 5 A1	<input checked="" type="checkbox"/>	Method and apparatus for managing a lock for an object	709/102
37	US 65781 37 B2	<input checked="" type="checkbox"/>	Branch and return on blocked load or store	712/228
38	US 65781 31 B1	<input checked="" type="checkbox"/>	Scaleable hash table for shared-memory multiprocessor system	711/216
39	US 65499 59 B1	<input checked="" type="checkbox"/>	Detecting modification to computer memory by a DMA device	710/22
40	US 65499 30 B1	<input checked="" type="checkbox"/>	Method for scheduling threads in a multithreaded processor	709/104
41	US 65464 43 B1	<input checked="" type="checkbox"/>	Concurrency-safe reader-writer lock with time out support	710/200
42	US 65429 91 B1	<input checked="" type="checkbox"/>	Multiple-thread processor with single-thread interface shared among threads	712/228
43	US 65359 05 B1	<input checked="" type="checkbox"/>	Method and apparatus for thread switching within a multithreaded processor	709/108
44	US 65079 03 B1	<input checked="" type="checkbox"/>	High performance non-blocking parallel storage manager for parallel software executing on coordinates	711/173
45	US 65078 62 B1	<input checked="" type="checkbox"/>	Switching method in a multi-threaded processor	709/107
46	US 64704 43 B1	<input checked="" type="checkbox"/>	Pipelined multi-thread processor selecting thread instruction in inter-stage buffer based on count information	712/205
47	US 64570 64 B1	<input checked="" type="checkbox"/>	Method and apparatus for detecting input directed to a thread in a multi-threaded process	709/318
48	US 64496 66 B2	<input checked="" type="checkbox"/>	One retrieval channel in a data controller having staging registers and a next pointer register and programming a context of a direct memory access block	710/23
49	US 64209 03 B1	<input checked="" type="checkbox"/>	High speed multiple-bit flip-flop	326/93
50	US 64184 60 B1	<input checked="" type="checkbox"/>	System and method for finding preempted threads in a multi-threaded application	709/108
51	US 63973 79 B1	<input checked="" type="checkbox"/>	Recording in a program execution profile references to a memory-mapped active device	717/140
52	US 63743 67 B1	<input checked="" type="checkbox"/>	Apparatus and method for monitoring a computer system to guide optimization	714/37
53	US 63634 11 B1	<input checked="" type="checkbox"/>	Intelligent network	709/202
54	US 63518 08 B1	<input checked="" type="checkbox"/>	Vertically and horizontally threaded processor with multidimensional storage for storing thread data	712/228
55	US 63413 47 B1	<input checked="" type="checkbox"/>	Thread switch logic in a multiple-thread processor	712/228
56	US 63361 50 B1	<input checked="" type="checkbox"/>	Apparatus and method for enhancing data transfer rates using transfer control blocks	710/5

	Docum ent ID	U	Title	Current OR
57	US 63245 94 B1	<input checked="" type="checkbox"/>	System for transferring data having a generator for generating a plurality of transfer extend entries in response to a plurality of commands received	710/5
58	US 62956 00 B1	<input checked="" type="checkbox"/>	Thread switch on blocked load or store using instruction thread field	712/228
59	US 62860 27 B1	<input checked="" type="checkbox"/>	Two step thread creation with register renaming	709/107
60	US 62693 91 B1	<input checked="" type="checkbox"/>	Multi-processor scheduling kernel	709/100
61	US 62498 86 B1	<input checked="" type="checkbox"/>	Computer system and computer implemented process for performing user-defined tests of a client-server system with run time compilation of test results	714/47
62	US 62370 73 B1	<input checked="" type="checkbox"/>	Method for providing virtual memory to physical memory page mapping in a computer operating system that randomly samples state information	711/202
63	US 62125 73 B1	<input checked="" type="checkbox"/>	Mechanism for invoking and servicing multiplexed messages with low context switching overhead	709/315
64	US 61957 48 B1	<input checked="" type="checkbox"/>	Apparatus for sampling instruction execution information in a processor pipeline	712/227
65	US 61956 76 B1	<input checked="" type="checkbox"/>	Method and apparatus for user side scheduling in a multiprocessor operating system program that implements distributive scheduling of processes	709/107
66	US 61758 14 B1	<input checked="" type="checkbox"/>	Apparatus for determining the instantaneous average number of instructions processed	702/182
67	US 61638 40 A	<input checked="" type="checkbox"/>	Method and apparatus for sampling multiple potentially concurrent instructions in a processor pipeline	712/227
68	US 61483 96 A	<input checked="" type="checkbox"/>	Apparatus for sampling path history in a processor pipeline	712/227
69	US 61190 75 A	<input checked="" type="checkbox"/>	Method for estimating statistics of properties of interactions processed by a processor pipeline	702/186
70	US 60921 80 A	<input checked="" type="checkbox"/>	Method for measuring latencies by randomly selected sampling of the instructions while the instruction are executed	712/200
71	US 60731 59 A	<input checked="" type="checkbox"/>	Thread properties attribute vector based thread selection in multithreading processor	709/103
72	US 60700 09 A	<input checked="" type="checkbox"/>	Method for estimating execution rates of program execution paths	717/130
73	US 60000 44 A	<input checked="" type="checkbox"/>	Apparatus for randomly sampling instructions in a processor pipeline	714/47
74	US 59648 67 A	<input checked="" type="checkbox"/>	Method for inserting memory prefetch operations based on measured latencies in a program optimizer	712/219
75	US 59336 27 A	<input checked="" type="checkbox"/>	Thread switch on blocked load or store using instruction thread field	712/228
76	US 59238 72 A	<input checked="" type="checkbox"/>	Apparatus for sampling instruction operand or result values in a processor pipeline	712/244
77	US 58729 63 A	<input checked="" type="checkbox"/>	Resumption of preempted non-privileged threads with no kernel intervention	712/233
78	US 58288 81 A	<input checked="" type="checkbox"/>	System and method for stack-based processing of multiple real-time audio tasks	709/314
79	US 58094 50 A	<input checked="" type="checkbox"/>	Method for estimating statistics of properties of instructions processed by a processor pipeline	702/186



	Docum ent ID	U	Title	Current OR
80	US 55353 61 A	<input checked="" type="checkbox"/>	Cache block replacement scheme based on directory control bit set/reset and hit/miss basis in a multiheading multiprocessor environment	711/145
81	US 54210 14 A	<input checked="" type="checkbox"/>	Method for controlling multi-thread operations issued by an initiator-type device to one or more target-type peripheral devices	709/100
82	US 52261 31 A	<input checked="" type="checkbox"/>	Sequencing and fan-out mechanism for causing a set of at least two sequential instructions to be performed in a dataflow processing computer	712/201
83	US 51971 38 A	<input checked="" type="checkbox"/>	Reporting delayed coprocessor exceptions to code threads having caused the exceptions by saving and restoring exception state during code thread switching	712/222
84	US 51797 02 A	<input type="checkbox"/>	System and method for controlling a highly parallel multiprocessor using an anarchy based scheduler for parallel execution thread scheduling	709/102

	Docum ent ID	U	Title	Current OR
1	US 20030 10119 0 A1	<input type="checkbox"/>	Schema-based notification service	707/100
2	US 20030 09748 5 A1	<input checked="" type="checkbox"/>	Schemas for a notification platform and related information services	709/313
3	US 20030 08885 4 A1	<input checked="" type="checkbox"/>	System and method for conditional tracing of computer programs	717/130
4	US 20030 03708 9 A1	<input checked="" type="checkbox"/>	Tracking operating system process and thread execution and virtual machine execution in hardware or in a virtual machine monitor	709/1
5	US 20030 00514 4 A1	<input checked="" type="checkbox"/>	EFFICIENT CLASSIFICATION MANIPULATION AND CONTROL OF NETWORK TRANSMISSIONS BY ASSOCIATING NETWORK FLOWS WITH RULE BASED FUNCTIONS	709/235
6	US 20030 00511 4 A1	<input checked="" type="checkbox"/>	Globally distributed load balancing	709/225
7	US 20030 00502 9 A1	<input checked="" type="checkbox"/>	Termination detection for shared-memory parallel programs	709/107
8	US 20030 00502 5 A1	<input checked="" type="checkbox"/>	Load-balancing queues employing LIFO/FIFO work stealing	709/102
9	US 20020 19917 2 A1	<input checked="" type="checkbox"/>	Dynamic instrumentation event trace system and methods	717/128
10	US 20020 16195 7 A1	<input checked="" type="checkbox"/>	Methods and systems for handling interrupts	710/260
11	US 20020 12066 3 A1	<input checked="" type="checkbox"/>	Method and apparatus for slack stealing with dynamic threads	709/103
12	US 20020 12066 1 A1	<input checked="" type="checkbox"/>	Methods and apparatus for sharing slack in a time-partitioned system	709/102
13	US 20020 10384 7 A1	<input checked="" type="checkbox"/>	Efficient mechanism for inter-thread communication within a multi-threaded computer system	709/107
14	US 20020 09986 7 A1	<input checked="" type="checkbox"/>	Portable operating environment for information devices	709/315
15	US 20020 09180 0 A1	<input checked="" type="checkbox"/>	Portable operating environment for information devices	709/219
16	US 20020 08794 9 A1	<input checked="" type="checkbox"/>	System and method for software diagnostics using a combination of visual and dynamic tracing	717/124
17	US 20020 07812 4 A1	<input checked="" type="checkbox"/>	Hardware-assisted method for scheduling threads using data cache locality	709/106

	Docum ent ID	U	Title	Current OR
18	US 20020 07095 1 A1	<input checked="" type="checkbox"/>	Portable operating environment for information devices	345/629
19	US 20020 05935 8 A1	<input checked="" type="checkbox"/>	Method and system for accessing objects of different thread types	709/107
20	US 20020 05783 7 A1	<input checked="" type="checkbox"/>	Portable operating environment for information devices	382/187
21	US 20020 05729 0 A1	<input checked="" type="checkbox"/>	Portable operating environment for information devices	345/744
22	US 20020 05596 5 A1	<input checked="" type="checkbox"/>	Method and system for accessing objects of different thread types	709/107
23	US 20020 03841 6 A1	<input checked="" type="checkbox"/>	System and method for reading and writing a thread state in a multithreaded central processing unit	712/228
24	US 20010 05645 6 A1	<input checked="" type="checkbox"/>	PRIORITY BASED SIMULTANEOUS MULTI-THREADING	709/103
25	US 20010 03477 1 A1	<input checked="" type="checkbox"/>	Network portal system and methods	709/217
26	US 65535 64 B1	<input checked="" type="checkbox"/>	Process and system for merging trace data for primarily interpreted methods	717/128
27	US 65465 46 B1	<input checked="" type="checkbox"/>	Integrating operating systems and run-time systems	717/114
28	US 65429 20 B1	<input checked="" type="checkbox"/>	Mechanism for implementing multiple thread pools in a computer system to optimize system performance	709/104
29	US 65429 19 B1	<input checked="" type="checkbox"/>	Operating system for use with protection domains in a single address-space	709/100
30	US 65299 62 B1	<input checked="" type="checkbox"/>	Preserving thread identity during remote calls	709/330
31	US 65196 36 B2	<input checked="" type="checkbox"/>	Efficient classification, manipulation, and control of network transmissions by associating network flows with rule based functions	709/223
32	US 65131 55 B1	<input checked="" type="checkbox"/>	Method and system for merging event-based data and sampled data into postprocessed trace output	717/124
33	US 64534 63 B1	<input checked="" type="checkbox"/>	Method and apparatus for providing finer marking granularity for fields within objects	717/127
34	US 64153 34 B1	<input checked="" type="checkbox"/>	Method and system for accessing objects of different thread types	709/316
35	US 64011 55 B1	<input checked="" type="checkbox"/>	Interrupt/software-controlled thread processing	710/266
36	US 64011 38 B1	<input checked="" type="checkbox"/>	Interface for patient context sharing and application switching	709/328
37	US 63634 10 B1	<input checked="" type="checkbox"/>	Method and system for threaded resource allocation and reclamation	709/104

	Docum ent ID	U	Title	Current OR
38	US 63145 30 B1	<input checked="" type="checkbox"/>	Processor having a trace access instruction to access on-chip trace memory	714/38
39	US 62893 69 B1	<input checked="" type="checkbox"/>	Affinity, locality, and load balancing in scheduling user program-level threads for execution by a computer system	709/103
40	US 62827 01 B1	<input checked="" type="checkbox"/>	System and method for monitoring and analyzing the execution of computer programs	717/125
41	US 62757 49 B1	<input checked="" type="checkbox"/>	Interrupt-controlled thread processing	700/292
42	US 62266 89 B1	<input checked="" type="checkbox"/>	Method and mechanism for interprocess communication using client and server listening threads	709/314
43	US 62021 99 B1	<input checked="" type="checkbox"/>	System and method for remotely analyzing the execution of computer programs	717/125
44	US 61580 45 A	<input checked="" type="checkbox"/>	Portable debugging services utilizing a client debugger object and a server debugger object with flexible addressing support	717/124
45	US 61515 69 A	<input checked="" type="checkbox"/>	Automated sequence of machine-performed attempts to unfreeze an apparently frozen application program	703/22
46	US 61345 94 A	<input checked="" type="checkbox"/>	Multi-user, multiple tier distributed application architecture with single-user access control of middle tier objects	709/229
47	US 61087 15 A	<input checked="" type="checkbox"/>	Method and system for invoking remote procedure calls	709/330
48	US 60701 98 A	<input checked="" type="checkbox"/>	Encryption with a streams-based protocol stack	709/321
49	US 60473 23 A	<input checked="" type="checkbox"/>	Creation and migration of distributed streams in clusters of networked computers	709/227
50	US 60289 99 A	<input checked="" type="checkbox"/>	System and method for non-sequential program statement execution with incomplete runtime information	717/134
51	US 60264 28 A	<input checked="" type="checkbox"/>	Object oriented thread context manager, method and computer program product for object oriented thread context management	709/108
52	US 60263 62 A	<input checked="" type="checkbox"/>	Tool and method for diagnosing and correcting errors in a computer program	705/1
53	US 59960 68 A	<input checked="" type="checkbox"/>	Method and apparatus for renaming registers corresponding to multiple thread identifications	712/228
54	US 59535 30 A	<input checked="" type="checkbox"/>	Method and apparatus for run-time memory access checking and memory leak detection of a multi-threaded program	717/127
55	US 59448 16 A	<input checked="" type="checkbox"/>	Microprocessor configured to execute multiple threads including interrupt service routines	712/215
56	US 59371 87 A	<input checked="" type="checkbox"/>	Method and apparatus for execution and preemption control of computer process entities	709/104
57	US 59110 60 A	<input checked="" type="checkbox"/>	Computer method and apparatus for unfreezing an apparently frozen application program being executed under control of an operating system	709/100
58	US 58901 89 A	<input checked="" type="checkbox"/>	Memory management and protection system for virtual memory in computer system	711/100
59	US 58451 29 A	<input checked="" type="checkbox"/>	Protection domains in a single address space	710/200
60	US 58387 66 A	<input checked="" type="checkbox"/>	System and method for providing shared resources to test platforms	379/9

	Docum ent ID	U	Title	Current OR
61	US 58156 53 A	<input checked="" type="checkbox"/>	Debugging system with portable debug environment-independent client and non-portable platform-specific server	714/38
62	US 57872 45 A	<input checked="" type="checkbox"/>	Portable debugging service utilizing a client debugger object and a server debugger object	714/38
63	US 56279 87 A	<input checked="" type="checkbox"/>	Memory management and protection system for virtual memory in computer system	711/200
64	US 56131 14 A	<input checked="" type="checkbox"/>	System and method for custom context switching	709/108
65	US 55903 26 A	<input checked="" type="checkbox"/>	Shared data management scheme using shared data locks for multi-threading	711/150
66	US 54817 19 A	<input checked="" type="checkbox"/>	Exception handling method and apparatus for a microkernel data processing system	709/108

	Docum ent ID	U	Title	Current OR
1	JP 20010 05983 A	<input type="checkbox"/>	PLOTTING CONTROLLER AND RECORDING MEDIUM	
2	NN940 8295	<input checked="" type="checkbox"/>	Inexpensive Mechanism to Identify Threads Uniquely From User Level	
3	NA910 9445	<input checked="" type="checkbox"/>	Sixteen-bit Operating System Fast Safe RAM Semaphore Compatibility in an Intel 80386 Paged Environment.	
4	US 20020 07812 4 A	<input checked="" type="checkbox"/>	Thread scheduling method in multi-processor computer system, involves identifying and scheduling threads having same id by mining patterns in entries for contiguous cache lines	
5	US 20020 00266 7 A	<input checked="" type="checkbox"/>	Embedded processor architecture for enabling multithreading, invokes zero-time context switches between end and beginning of program instruction execution states in threads	
6	JP 20010 05983 A	<input checked="" type="checkbox"/>	Patterning control device includes setting unit which sets produced context as context class, during starting of thread	
7	JP 10232 787 A	<input type="checkbox"/>	Resource manager access method in distributed calculation system - involves searching context using thread identifier as key from context management table	

	Docum ent ID	U	Title	Current OR
1	JP 04215 138 A	<input type="checkbox"/>	THREAD SWITCHING SYSTEM FOR WRITE WAIT PROCESS	
2	NNRD4 29129	<input checked="" type="checkbox"/>	Runtime Translation of Java Based APIs for Use with Native Applications	
3	NN960 129	<input checked="" type="checkbox"/>	Deadlock Detection Using Only Thread Identifiers	
4	NN930 5283	<input type="checkbox"/>	Multisequencing a Single Instruction Stream - Decoding Virtual Processors Out of Sequence	